

Form PTO-1449 U.S. DEPARTMENT OF COMMERCE (REV. 8-83) PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE CITATION <small>(Use several sheets if necessary)</small>				ATTY. DOCKET NO. BUR9200000163US1		SERIAL NO.	
				APPLICANT Combs, et al.		<small>1C 28240 MAIL</small> <small>SEE 13 201</small> <small>RECEIVED</small>	
				FILING DATE			
				GROUP 2824			
U. S. PATENT DOCUMENTS							
EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLAS S	SUB- CLASS	MAILING DATE IF APPROPRIATE
<i>MJ</i>		6,058,496	05/2000	Gillis, et al.	—	—	—
		5,923,676	07/1999	Sunter, et al.	—	—	—
		5,736,862	04/1998	Hamblin	—	—	—
		5,502,392	03/1996	Arjavalingam, et al.	—	—	—
		5,278,841	01/1994	Myers	—	—	—
		5,268,645	12/1993	Prokoff, et al.	—	—	—
		5,266,901	11/1993	Woo	—	—	—
<i>MJ</i>		4,565,966	01/1986	Burr, et al.	—	—	—
FOREIGN PATENT DOCUMENTS							
	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLAS S	SUB- CLASS	TRANSLATION
							YES
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
<i>MJ</i>		IBM Technical Disclosure Bulletin, Vol. 37, No. 9, September 1994, "Interconnect Verification Method for Boards, pp. 299-302.					
		IBM Technical Disclosure Bulletin, Vol. 8, No. 11, April, 1966, "Measurement of Impedances Under Dynamic Pulse Conditions", pp.1507-1510.					
EXAMINER				DATE CONSIDERED			
<i>MJ</i>				7/01/03			
<small>*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</small>							